

# UX101 Connectors and Jumpers

Rev B

P1 DVI-I  
Analog RGB or Digital single link  
Use Molex cable 88741-8000 (or equivalent) for single link digital input  
Use Molex cable 88741-8300 (or equivalent) for analog input

J2 Composite Video – RCA

J1 S-Video Mini-DIN 4

J3 +12VDC power input  
2.5mm ID / 5.5mm OD

P11 Aux Power Input  
PIN FUNCTION  
1 +12VDC  
2 Ground  
3 Ground  
4 +5VDC

P11 can be used to provide higher current supply voltages to a panel.

If P11-pin4 is used to supply +5V, remove jumper at JP2.

If P11-pin1 is used to supply the +12V to the system do not apply power to J3.

If dual +12V supplies are desired, remove jumper at JP3. In this case the board gets its power from J3 and the panel gets power from P11.

P9 Panel/Backlight (unswitched)  
Hirose DF11-12DP-2DS(22)  
Mates with Hirose DF11-12DF-2C (or equivalent)

PIN	FUNCTION
1	Ground
2	Ground
3	Ground
4	Ground
5	Ground
6	Ground
7	+5VDC
8	+5VDC
9	+5VDC
10	+5VDC
11	+5VDC
12	+5VDC

Maximum .75A per pin. Total maximum depends on board configuration and external power supply capacity.

P7 Panel/ Backlight  
Hirose DF11-14DP-2DS(22)  
Mates with Hirose DF11-14DF-2C (or equivalent)

PIN	FUNCTION
1	BLON+ *
2	BLON- *
3	Ground
4	Ground
5	Ground
6	Ground
7	Ground
8	Ground
9	+12VDC
10	+12VDC
11	+12VDC
12	+12VDC
13	+12VDC
14	+12VDC

\* BLON+ and BLON- are typically used to turn on a backlight inverter. LVTTTL compatible. Either can be programmed for PWM control of the backlight inverter.

Maximum .75A per pin. Total maximum depends on board configuration and external power supply capacity.

P8 Panel LVDS Data  
Hirose DF11-32DP-2DS(22)  
Mates with Hirose DF11-32DF-2C (or equivalent)

<u>PIN</u>	<u>FUNCTION</u>
1	PNLPWRON- *
2	PNLPWRON+ *
3	Ground
4	Ground
5	TXBCLK+
6	TXBCLK-
7	TXB3+
8	TXB3-
9	TXB2+
10	TXB2-
11	TXB1+
12	TXB1-
13	TXB0+
14	TXB0-
15	TXA3+
16	TXA3-
17	TXACLK+
18	TXACLK-
19	TXA2+
20	TXA2-
21	TXA1+
22	TXA1-
23	TXA0+
24	TXA0-
25	Ground
26	Ground
27	Ground
28	Ground
29	SWPNLPWR
30	SWPNLPWR
31	SWPNLPWR
32	SWPNLPWR

\* PNLPWON+ and PNLPWON- can be used to turn on panel power. PNLPWON+ is positive true and PNLPWON- is negative true. LVTTTL compatible.

SWPNLPWR is a switched panel power supply voltage. The voltage is selectable via JP4 for either 3.3V, 5V or 12V panel power. SWPNLPWR is switched on and off under software control and is on when PNLPWON+/- are active. Maximum current is .75A per pin for a total of 3.0A if all 4 pins are used.

The TXA LVDS channel is used to transmit the first pixel (left most as viewed the display) and TXB channel transmits the second (right most) pixel in dual pixel per clock mode of operation. The TXACLK and TXBCLK pairs are the same in the dual pixel per clock mode.

For single pixel per clock operation all data are transferred on the TXA channel and the TXB channel is unused.

The LVDS driver used is a National DS90C387. H and V sync signals are transmitted along with DATAEN and clock.

### UX101 Board Outline

